

WHAT IS CLAIMED IS

*Sub A1*  
1. A biasing/erasing oscillator in a magnetic tape recording apparatus having an entire-width erasing head for erasing signals recorded in an azimuth track, or the azimuth track and a linear track, of a magnetic tape, and a linear record erasing head for erasing signals recorded in the linear track of the magnetic tape, comprising:

5 a series circuit connecting in series through a series junction point between at least one of said entire-width erasing head and said linear record erasing head and a inductance element:

an oscillating capacitor connected in parallel with said series circuit;

10 a transistor having a collector, base and an emitter connected to said series junction point;

a direct current preventive capacitor connected between said one end of said series circuit and said base; and

a bias resistor for given an bias voltage to said base.

15 2. An oscillator according to claim 1, wherein said inductance element is a *remaining one of* remainder other than said entire-width erasing head and said linear record erasing head.

3. An oscillator according to claim 1, wherein said inductance element is an inductor.

*A* 20 4. An oscillator according to claim 3, wherein said series circuit includes two erasing heads of *said* ~~head~~ entire-width erasing head and said linear record erasing head and said inductor, said series junction point is a junction point between said two erasing heads and said inductor.